

REMARKS

The Office Action dated April 20, 2005 has been fully considered by the Applicant. Claims 1, 2, 10, 11, 13 and 14 are currently amended; claims 3, 4, 5, 9 and 12 have been previously presented; and claims 6-8 have been canceled.

Attached is a Petition To Request Two-Month Extension of Time and a check for \$450 to cover the cost of the Extension.

Claims 4-5 and 10-12 have been withdrawn from allowance and now rejected in view of the newly cited United Patent No. 6,377,275. Applicant sincerely requests reconsideration of the rejection based on the currently amended claims herein.

Claims 1-2, 6-14 have been rejected under 35 USC 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. The claims have been amended to include the proper antecedent basis as required.

Claims 1-2, 6-14 have been rejected under 35 USC 103 as being unpatentable over United States Patent No. 6,611,270 to Hosotani in view of United States Patent No. 6,377,275 to Kim. Applicant sincerely believes that the claims as currently amended are novel over the cited references and respectfully requests reconsideration of the rejection.

Claim 1 as currently amended is directed toward a system for the control of the generation of an on-screen display comprising a control means which continues to operate the system upon movement of an area of a first on-screen display until a request or deletion of a region of the first on-screen display until a request to draw into a data buffer memory of the display or create a new region on the display is received respectively, where upon the control means detects whether or not a vertical synchronization signal for the display screen has occurred in an interim period since the

movement of the area or deletion of the region of the first display. If the vertical synchronization signal has occurred in the interim period, the drawing into the data buffer memory or creation of the new region with regard to the on-screen display occurs immediately. Applicant sincerely believes that the cited references do not include the features in Applicant's currently amended claim 1.

Applicant's currently amended claim 1 includes the provision of detecting whether or not a vertical synchronization signal occurred in an interim period since the movement of the area or the deletion of the region of the first display. If the vertical synchronization signal has occurred in the interim period, the drawing into the data buffer memory or creation of the new region occurs immediately. The '270 patent to Hosotani does not disclose that a delay in waiting for the next vscyn signal can be avoided, as does Applicant's invention, nor does the '270 patent teach toward a solution to check whether a vertical synchronization signal occurred in the interim period, as does Applicant's currently amended claim 1. Therefore, Applicant sincerely believes that currently amended claim 1 is novel over the '270 patent to Hosotani and respectfully requests reconsideration of the rejection.

In addition, the '270 patent to Hosotani merely operates in accordance with the state of the art as described in Applicant's specification on Page 2. The '270 patent is concerned with improvement to processing efficiency in relation to an on-screen display, in particular by controlling access to the OSD-RAM. However, in the '270 patent a vscync signal is required before access to the OSD-RAM is allowed. Further, there is no discussion in the '270 patent to Hosotani that the delay in awaiting a vsync signal can be avoided. As stated in the '270 patent, Col 10, lines 63-67, "a display signal is produced in the OSD logic or circuit according to the display data in synchronization with vertical and horizontal synchronization signals..." This, therefore, suggests that

the display signal in the ‘270 patent can be transmitted upon receipt of a vsync signal (i.e. that a change of the display must await the next vsync signal). Thus, the ‘270 patent does not disclose that the delay in waiting for the next vsync signal can be avoided, nor does the ‘270 patent direct a person skilled in the art to the solution as presented in Applicant’s currently amended claim 1 which involves checking to see if a vertical synchronization sign occurred in the interim period. Therefore, Applicant sincerely believes that currently amended claim 1 is novel over the ‘270 patent to Hosotani and respectfully requests reconsideration of the rejection.

United States Patent No. 6,377,275 is directed toward outputting a display on a display device. However, the ‘275 patent merely waits the next vsync signal in order to effect a change in the on-screen display. In particular, the ‘275 patent discloses the necessity to move the display intermittently around the display screen, such that the display does not remain in the same position, which could potentially damage the screen. Unlike in Applicant’s invention, the predetermined time referred to in the ‘275 patent is merely the time for which the display is allowed to remain in the same position on screen without causing damage to the screen. The time delay in Applicant’s invention is caused by waiting for a vsync signal. In addition, the ‘275 patent makes no mention of glitches that may occur if access to the data buffer memory is not controlled. The ‘275 patent does not mention an inconvenient time delay required in awaiting the next vsync signal. Therefore, Applicant sincerely believes that neither of the cited references, either alone or in combination, render obvious the features of Applicant’s currently amended claims 1. Reconsideration of the rejection is respectfully requested.

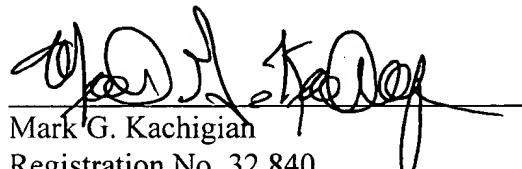
Independent claims 13 and 14 have been currently amended to include the provision of the control means detecting whether or not a vertical synchronization signal for the display screen occurs

in an interim period. The cited references do not teach a system for the control of the generation of an on-screen display comprising control means for the display which continue to operate the system upon movement of an area of a first on-screen display or deletion of a region of the first on-screen display until a request to draw into a data buffer memory of the display or create a new region on the display is received respectively and whereupon the control means detects whether or not a vertical synchronization signal for the display screen occurred in an interim period. Therefore, Applicant respectfully requests reconsideration of the rejection.

It is believed that the application is now in condition for allowance and such action is earnestly solicited. If any further issues remain, a telephone conference with the Examiner is respectfully requested.

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Respectfully submitted,



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Date: 20 September 2005